

Bruna Arruda Araujo

Profession

- M.Sc. student in [Electronic Systems Engineering](#) at [Federal University of Santa Catarina \(UFSC\)](#), campus Joinville.
 - Holds a [CAPES](#) master level scholarship at the Software/Hardware Integration Lab (LISHA) at UFSC.
- B.Eng. in Eletronic Engineering from the [Federal University of Pelotas \(UFPel\)](#).
- [Curriculum Vitae at CNPq \(Lattes\)](#).

Research Activities

Research Topics

- RISC V
- Real Time Systems
- PULP